UNITED STATES PATENT APPLICATION

OF

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AND

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FOR METHOD FOR MANUFACTURING LIQUID CRYSTAL DISPLAY DEVICE

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[0001] This application claims the benefit of the Korean Application No. P2002-74819 filed on November 28, 2002, which is hereby incorporated by reference for all purposes as if fully set forth herein. This application incorporates by reference two co-pending applications, serial number 10/184,096, filed on June 28, 2002, entitled "SYSTEM AND METHOD FOR MANUFACTURING LIQUID CRYSTAL DISPLAY DEVICES" (Attorney Docket Number 8733.666.00) and serial number 10/184,088, filed on June 28, 2002, entitled "SYSTEM FOR FABRICATING LIQUID CRYSTAL DISPLAY AND METHOD OF FABRICATING LIQUID CRYSTAL DISPLAY AND METHOD OF FABRICATING LIQUID CRYSTAL DISPLAY USING THE SAME" (Attorney Docket Number 8733.684.00), as if fully set forth herein.

BACKGROUND OF THE INVENTION

Field of the Invention

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[0002] The present invention relates to a liquid crystal display (LCD) device, and more particularly, to a method for manufacturing an LCD device while decreasing a tact time.

Discussion of the Related Art

[0003] With the expansion of the information society, a need has arisen for displays capable of producing high quality images in thin, lightweight packages and that consume little power. To meet such needs, research has produced a variety of flat panel display devices, including liquid crystal displays (LCD), plasma displays (PDP), electro luminescent displays (ELD), and vacuum fluorescent displays (VFD). Some of these display technologies have already been applied in information displays.

[0004] Of the various types of flat panel display devices, LCDs are very widely used. In fact, in portable devices, such as notebook PC computers, LCD

technology has already replaced cathode ray tubes (CRT) as the display of choice. Moreover, even in desktop PCs and in TV monitors, LCDs devices are becoming more common.

[0005] Despite various technical developments in LCD technology, however, research in enhancing picture quality of LCD devices has been lacking compared to research in other features and advantages of LCD devices. Therefore, to increase the use of LCD devices as displays in various fields of application, LCD devices capable of expressing high quality images (e.g., images having a high resolution and a high luminance) with large-sized screens, while still maintaining a light weight, minimal dimensions, and low power consumption must be developed.

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[0006] LCDs generally include an LCD panel for displaying a picture and a driving part for providing driving signals to the liquid crystal display panel.

[0007] Typically, LCD panels include first and second glass substrates bonded to each other while being spaced apart by a cell gap, wherein a layer of liquid crystal material is injected into the cell gap.

[0008] The first glass substrate (i.e., thin film transistor (TFT) array substrate), supports a plurality of gate lines spaced apart from each other at a fixed interval and extending along a first direction; a plurality of data lines spaced apart from each other at a fixed interval and extending along a second direction, substantially perpendicular to the first direction, wherein pixel regions are defined by crossings of the gate and data lines; a plurality of pixel electrodes arranged in a matrix pattern within respective ones of the pixel regions; and a plurality of thin film transistors (TFTs) capable of transmitting signal from the data lines to corresponding ones of the pixel electrodes in response to a signal applied to respective ones of the gate lines.

[0009] The second glass substrate (i.e., color filter substrate) supports a black matrix layer for preventing light leakage in areas outside the pixel regions; a color filter layer (R, G, B) for selectively transmitting light having predetermined wavelengths; and a common electrode for displaying a picture. Common electrodes of In-Plane Switching (IPS) mode LCD devices, however, are formed on the first substrate.

[0010] Uniformity of the cell gap is maintained by spacers arranged between the first and second glass substrates, bonded together by a seal pattern. The seal pattern includes a liquid crystal injection inlet allowing liquid crystal material to be injected into the cell gap. Upon injecting liquid crystal material into the cell gap via the liquid crystal injection inlet, the layer of liquid crystal material is thus formed.

[0011] The layer of liquid crystal material is driven (e.g., light transmittance characteristics of the layer of liquid crystal material are controlled) according to electric fields generated between the first and second substrates by the pixel electrode and the common electrode. By controlling the light transmittance characteristics of the layer of liquid crystal material, pictures may be displayed.

[0012] To form the aforementioned layer of liquid crystal material, related art manufacturing methods incorporate a liquid crystal injection method wherein a pressure difference is created between the cell gap and a vacuum chamber and liquid crystal material is injected into the liquid crystal injection inlet via a capillary phenomenon. A method for manufacturing the related art LCD device incorporating the liquid crystal injection method will now be described.

[0013] The first substrate (i.e., a TFT array substrate), supporting the TFTs and pixel electrodes, and the second substrate (i.e., the color filter substrate), supporting the black matrix layer, color filter layer, and common electrode, are provided. Next,

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spacers are dispersed on the TFT array substrate to maintain a uniform cell gap between the two substrates. A seal pattern is then formed at a periphery of the other of the two substrates to prevent liquid crystal material from leaking and to bond the two substrates together. The seal pattern is typically formed of a thermo-hardening material such as an epoxy including a mixture of an epoxy resin and an initiator. Next, a heat treatment is performed to bond the TFT array and color filter substrates to each other. When performing the heat treatment, the epoxy resin within the epoxy seal pattern is activated by the initiator and becomes a highly cross-linked polymer. As a result, the epoxy seal pattern functions as the seal pattern having suitable adhesion characteristics.

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[0014] Subsequently, the bonded substrates are placed in a vacuum chamber, wherein the cell gap between the bonded substrates is maintained in a vacuum state, and dipped into a reservoir of liquid crystal material. Since a vacuum is maintained within the cell gap, liquid crystal material is injected into the cell gap by a capillary phenomenon. After a predetermined amount of liquid crystal material has been injected into the cell gap, nitrogen gas (N₂) is pumped into the vacuum chamber, so that liquid crystal material is injected into regions of the cell gap not previously injected into, according to the pressure difference between the cell gap and the pressure within the vacuum chamber having the pumped nitrogen gas (N₂). As a result, the layer of liquid crystal material is formed between the bonded TFT array and color filter substrates.

[0015] Figure 1 illustrates a plan view illustrating a related art LCD device.

[0016] Referring to Figure 1, the first substrate (i.e., the TFT array substrate) supports a plurality of gate lines 11 spaced apart from each other at a fixed interval and extending along a first direction and a plurality of data lines 12 spaced apart from

each other at a fixed interval and extending along a second direction, substantially perpendicular to the first direction. Pixel regions P are defined by crossings of the gate and data lines 11 and 12, respectively. A plurality of pixel electrodes 16 are arranged in a matrix pattern within respective ones of the pixel regions P, and thin film transistors are formed at crossings of the plurality of gate and data lines 11 and 12, respectively. In response to signals applied from the gate lines 11, the thin film transistors transmit signals applied from the data lines 12 to respective ones of the pixel electrodes 16.

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[0017] Each the thin film transistor includes a gate electrode 13 protruding from a corresponding gate line 11, a gate insulating layer (not shown) formed over an entire surface of the first substrate 10, a semiconductor layer 15 formed on the gate insulating layer in a region above the gate electrode 13, a source electrode 15a protruding from a corresponding data line 12, and a drain electrode 15b formed opposite the source electrode 15a by a predetermined distance and electrically connected to the pixel electrode 16 through a contact hole 17.

[0018] The second substrate (i.e., the color filter substrate; not shown) supports a black matrix layer having openings in regions corresponding to the pixel regions P of the first substrate 10 and prevents light leakage; an R/G/B color filter layer for selectively transmitting light having predetermined wavelengths; and a common electrode for driving the layer of liquid crystal material with the pixel electrodes 16.

[0019] The first substrate 10 is bonded to the second substrate (i.e., the color filter substrate), wherein the two substrates are spaced apart from each other by a predetermined distance, uniformly maintained by spacers. The first and second substrates are bonded to each other using a seal pattern having a liquid crystal

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injection inlet. Upon injecting liquid crystal material into the cell gap via the liquid crystal injection inlet, the layer of liquid crystal material is thus formed.

[0020] Figure 2A illustrates a method of forming a seal pattern according to a screen printing method.

mask 32 having an opening 31 for selectively exposing a seal pattern region and a

squeegee 33 for forming the seal pattern on the first substrate 10 by selectively

providing sealant material to the first substrate 10 via the screen mask 32. After

arranging the screen mask 32 on the first substrate 10, sealant material is disposed and

the seal pattern is formed on the first substrate 10 in regions corresponding to the

opening 31 by rolling a squeegee 33 over the first substrate 10 along the arrow

direction in the drawing. Next, solvent within the seal pattern is evaporated in a

drying process for leveling. Further, the seal pattern includes a liquid crystal injection

inlet at one side thereof and is arranged at a periphery of a picture display region to

[0021] Referring to Figure 2A, a screen printing apparatus includes a screen

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prevent liquid crystal from leaking. [0022] The aforementioned related art screen printing method is a relatively simple process to perform. However, use of the aforementioned related art screen printing method is problematic because the amount of sealant material used can be excessive. More specifically, while sealant material is disposed over the entire surface of the screen mask 32, only a small portion of the disposed sealant material is actually incorporated into the seal pattern by the squeegee 33. Further, the screen mask 32 contacts the first substrate 10. Such contact generates defects within an alignment layer (not shown) formed on the first substrate 10 because the screen mask 32 damages the alignment layer. Accordingly, a picture quality of the related art LCD device becomes deteriorated.

[0023] In order to solve the problems arising from use of the aforementioned related art screen printing method, a seal dispensing method has been proposed.

[0024] Figure 2B illustrates a method of forming a seal pattern according to a dispensing method.

[0025] Referring to Figure 2B, the first substrate 10 (i.e., the TFT array substrate) is loaded onto a stage (not shown) capable of moving in many directions. Next, sealant material 7 is selectively dispensed along the periphery of the first substrate 10 via a syringe apparatus 34. Though not shown in Figure 2B (or 2A), the seal pattern 7 is typically divided into a main seal pattern and a dummy seal pattern wherein the dummy seal pattern is formed to protect the main seal pattern and to prevent liquid crystal material from leaking. Upon selectively dispensing sealant material along the periphery of the first substrate 10, a seal pattern is formed along the periphery of the first substrate 10. Using the aforementioned related art dispensing method, the amount of sealant material used may be decreased compared to the aforementioned related art screen printing method since sealant material is selectively dispensed along the periphery of the first substrate 10. Further, the syringe apparatus 34 does not contact the first substrate 10 such that damage to the alignment layer is substantially avoided and the picture quality of the LCD device is improved.

[0026] Use of the aforementioned related art dispensing method of forming the seal pattern is problematic, however, because the main and dummy seal patterns are formed of highly viscous sealant material. Accordingly, a nozzle of the syringe apparatus 34 becomes sticky with sealant material and an excessive amount of sealant material is accumulated on the nozzle. Subsequently, the excessive amount of sealant material accumulated on the nozzle becomes excessively dispensed onto the first substrate. During bonding of the first and second substrates, the excessively dispensed

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sealant material spreads toward an active region (center portion of the first substrate) and a dummy region (periphery of the first substrate) and the liquid crystal material becomes contaminated by the sealant material, thereby decreasing device reliability.

[0027] Further, use of the related art seal pattern formation methods may become difficult when the size of the first substrate increases, or when a size of the picture display area of the substrate changes (e.g., upon a model change of the liquid crystal display panel). In light of recent demand for liquid crystal display panels having increased size, the size of the first substrate has increased. Accordingly, positions where seal patterns are formed on the substrate change.

[0028] In the aforementioned related art dispensing method, if the positions where the seal pattern is formed on the substrate changes, the syringe apparatus must be disassembled and reassembled. As the size of liquid crystal display panels increase, the amount of time required to dispense a seal pattern having an increased size also increases. Accordingly, the tact time increases and the yield decreases.

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SUMMARY OF THE INVENTION

[0029] Accordingly, the present invention is directed to a method for manufacturing a liquid crystal display device that substantially obviates one or more problems due to limitations and disadvantages of the related art.

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[0030] An advantage of the present invention provides a method for manufacturing a liquid crystal display device having an improved device reliability and decreased tact time.

[0031] Another advantage of the present invention provides a method for manufacturing a liquid crystal display device capable of being applied to Multi-Model-on-a-Glass (MMG) models of liquid crystal display panels having various sizes.

[0032] Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. These and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

[0033] To achieve these and other advantages and in accordance with the purpose of the invention, as embodied and broadly described herein, a method for manufacturing an liquid crystal display device may, for example, include providing first and second substrates; forming a main sealant pattern on one of the first and second substrates via a printing method; forming a dummy sealant pattern on the substrate on which the main sealant pattern was formed via a dispensing method; dispensing liquid crystal material directly onto one of the first and second substrates; attaching the first and second substrates together; and irradiating the attached first and second substrates with UV light.

[0034] It is to be understood that both the foregoing general description and the following detailed description of the present invention are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

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[0035] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this application, illustrate embodiment(s) of the invention and together with the description serve to explain the principle of the invention.

[0036] In the drawings:

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[0037] Figure 1 illustrates a plan view illustrating a related art LCD device;

[0038] Figure 2A illustrates a method of forming a seal pattern according to a screen printing method;

[0039] Figure 2B illustrates a method of forming a seal pattern according to a dispensing method;

[0040] Figures 3A to 3F illustrate process steps of manufacturing an LCD device according to the principles of the present invention;

[0041] Figures 4A to 4C illustrate dummy seal patterns according to the principles of the present invention;

[0042] Figures 5A to 5C illustrate a method of forming a main seal pattern according to a screen printing method in accordance with the principles of the present invention;

[0043] Figure 6A illustrates a plan view of a one piece screen mask;

[0044] Figure 6B illustrates a plan view of a division screen mask;

[0045] Figure 7 illustrates an exemplary MMG model including a plurality of different sized LCD panels disposed within the same base substrate; and

[0046] Figure 8 illustrates UV fixation of a seal pattern according to the principles of the present invention.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0047] Reference will now be made in detail to the exemplary embodiments of the present invention, examples of which are illustrated in the accompanying drawings. Wherever possible, the same reference numbers will be used throughout the drawings to refer to the same or like parts.

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[0048] As sizes of liquid crystal display panels increase, the related art method of manufacturing LCD devices incorporating the liquid crystal injection method has become problematic. More specifically, the amount of time required to completely inject liquid crystal material between two substrates increases as the size of the LCD panel increases. Accordingly, the yield with which LCD devices are manufactured decrease. In order to solve the aforementioned problem, a liquid crystal dispensing method has been proposed in accordance with the principles of the present invention.

[0049] A method for manufacturing LCD devices using the aforementioned liquid crystal dispensing method will now be described in greater detail.

[0050] Figures 3A to 3F illustrate process steps of manufacturing an LCD device according to the principles of the present invention.

[0051] Referring to Figures 3A to 3F, four LCD panels may, for example, be formed within the same base substrate. In one aspect of the present invention, the number of LCD panels formed within the same substrate may be varied according to the size of the substrate.

[0052] As shown in Figure 3A, first and second substrates 30 and 20, respectively, may be prepared as required. Although not shown, a plurality of gate lines and a plurality of data lines crossing the gate lines may, for example, be formed on the first substrate 20 wherein a plurality of pixel regions may be defined by the crossings of the gate and data lines. Thin film transistors (TFTs) may be formed at crossings of the gate and data lines, wherein each TFT may include a gate electrode, a gate insulating layer, a semiconductor layer, an ohmic contact layer, source and drain electrodes, and a passivation layer. Further, pixel electrodes may be formed within respective ones of the plurality of pixel regions and may be connected to corresponding ones of the TFTs.

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[0053] In one aspect of the present invention, an alignment layer may be formed on the pixel electrodes to impart an initial alignment direction to molecules within subsequently dispensed liquid crystal material. In one aspect of the present invention, the alignment layer may be formed of a polyamide or polyimide group compound material, polyvinylalcohol (PVA), polyamic acid, and the like. In another aspect of the present invention, the alignment direction of the alignment layer may provided via a rubbing process. In one aspect of the present invention, the alignment layer may be formed of any material having a photoreactive group including, for example, polyvinylcinnamate (PVCN), polysiloxanecinnamate (PSCN), cellulosecinnamate (CelCN), and the like. In another aspect of the present invention, the alignment direction of the alignment layer may provided via a light exposure process.

[0054] Although not shown, the second substrate 30 may, for example, support a black matrix layer, an R/G/B color filter layer, and a common electrode. The black matrix layer may substantially prevent light from leaking to the gate lines, the data lines and the TFTs. The R/G/B color filter layer may be formed on the black matrix layer and the common electrode may be formed on the color filter layer. In one aspect of the present invention, an overcoat layer may be formed between the color filter layer and the common electrode. In one aspect of the present invention, the aforementioned alignment layer may also be formed on the common electrode.

[0055] According to the principles of the present invention, silver (Ag) paste may be formed at the periphery of the first substrate 20 to facilitate the application of a voltage to the common electrode of a bonded second substrate 30. In one aspect of the present invention, the first and second substrates 20 and 30, respectively, may form an In-Plane Switching (IPS) mode LCD device. Therefore, both the common electrode

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and the pixel electrodes may be formed on the first substrate 20 to induce an in-plane electric field and formation of the silver (Ag) paste may not be necessary.

[0056] As shown in Figure 3B, the liquid crystal material 40 may be dispensed directly onto the first substrate 20, thereby forming a layer of liquid crystal material. Subsequently, as shown in Figure 3C, a main seal pattern 70 may be formed on the second substrate 30 via a screen printing method.

[0057] Referring to Figure 3D, a dummy seal pattern 60 may be formed in a dummy region at the periphery of the second substrate 30 in a dispensing method using a dispensing apparatus 50. In one aspect of the present invention, the dummy seal pattern 60 may be formed to protect the main seal pattern 70 and to maintain a vacuum state of a cell gap between the first and second substrates during a subsequent bonding process. In another aspect of the present invention, the dummy seal pattern 60 may be formed substantially within any portion of the dummy region of the second substrate 30. Further, the dummy seal pattern 60 may be formed either after or before formation of the main seal pattern 70. While it has been shown in Figure 3D that the dummy seal pattern 60 may be provided as a closed shape, the principles of the present invention may be readily extended to provide the dummy seal pattern 60 in substantially any shape, examples of which will be described in greater detail below with reference to Figures 4A to 4C.

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[0058] According to the principles of the present invention, the main and dummy seal patterns 70 and 60, respectively, may be formed out of a sealant material that hardens upon exposure to UV light. Accordingly, in one aspect of the present invention, at least one of the main and dummy seal patterns 70 and 60 may be formed out of a sealant material including monomers or oligomers each having both ends coupled with an acrylic group mixed with an initiator. In another aspect of the present

invention, at least one of the main and dummy seal patterns 70 and 60 may be formed out of a selant material including or monomers or oligomers each having one end coupled with an acrylic group and the other end coupled with an epoxy group mixed with an initiator.

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[0059] As mentioned above, when the main seal pattern 70 contacts with the liquid crystal material 40 before hardening, the liquid crystal material 40 may become contaminated. Accordingly, the liquid crystal material 40 may be dispensed at the center portion of the first substrate 20. The liquid crystal material 40 dispensed at the center portion of the first substrate 20 may spread over the entire surface of the substrate until the main seal pattern 70 is hardened and after the main seal pattern 70 is hardened, such that a uniform amount of liquid crystal material is provided over the entire surface of the bonded substrates. In one aspect of the present invention, the liquid crystal material 40 may be dispensed on a different substrate than the main and dummy seal patterns 70 and 60. For example, the liquid crystal material 40 may be dispensed on the second substrate 30 while the main and dummy seal patterns 70 and 60 may be formed on the first substrate 20. Forming the liquid crystal material 40 and the main and dummy seal patterns 70 and 60 on different substrates may, however, increase the time required to manufacture the LCD because the processes required to form the main and dummy seal patterns 70 and 60 may be different from the amount of time required to dispense the liquid crystal material 40. Accordingly, in another aspect of the present invention, the liquid crystal material 40 and the main and dummy seal patterns 70 and 60 may be formed on the same substrate. Forming the liquid crystal material 40 and the main and dummy seal patterns 70 and 60 on the same substrates may make it difficult to clean the substrate because the seal patterns may be contaminated prior to bonding the two substrates together. Accordingly, and with

reference to Figure 3D, after forming the main and dummy seal patterns 70 and 60 on the second substrate 30, the second substrate 30 may be cleaned prior to the bonding process described below.

[0060] Although not shown, spacers may be formed on any one of the first and second substrates 20 and 30 to maintaining a substantially uniform cell gap between the first and second substrates 20 and 30. In one aspect of the present invention, the spacers may be formed on the second substrate 30. In another aspect of the present invention, the spacers may be provided as ball-type spacers, wherein ball-type spacers may be mixed into a solution at a predetermined density. Subsequently, the solution having the mixed-in ball-type spacers may be sprayed onto the substrate via a high pressure spraying nozzle. If ball-type spacers are applied to large-sized LCD devices, it may become difficult to maintain a uniform cell gap between the first and second substrates. Therefore, and in accordance with another aspect of the present invention, the spacers may be provided as column-type spacers such that column-type spacers may be formed over the substrate in regions corresponding to one of the gate lines, the common line, and the data lines. In one aspect of the present invention, column-type spacers may be formed on a base substrate having large-sized LCD devices disposed therein via photolithographic processes. In another aspect of the present invention the column-type spacers may be formed of a material such as a photosensitive organic resin (e.g., photo-acrylate, Benzocyclobutene (BCB), or polyimide, or the like).

[0061] As shown in Figure 3E, the first and second substrates 30 and 20 may be bonded together. During the bonding, the substrate onto which the liquid crystal was dispensed may be arranged beneath the other substrate such that the surface of the substrate directly contacting the dispensed liquid crystal material 40 faces toward the other substrate. The substrate onto which the seal patterns are formed may be

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arranged over the other substrate such that the surface of the substrate directly contacting the seal patterns faces toward the other substrate. Accordingly, the surfaces of the two substrates in direct contact with the liquid crystal material and the seal patterns oppose each other. In one aspect of the present invention, the bonding may be performed by pressing the uppermost substrate toward the lowermost substrate. In another aspect of the present invention, the bonding may be performed by releasing a vacuum state maintained between the two substrates when they are separated from each other.

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[0062] As shown in Figure 3F, the bonded first and second substrates 20 and 30 may be irradiated with UV light via a UV irradiation apparatus 80. Upon irradiating the UV light, monomer or oligomer material within the seal patterns may become polymerized by the initiator within the seal pattern and the first and second substrates 20 and 30 may be bonded together. In one aspect of the present invention, the seal pattern may include monomers or oligomers each having one end coupled to the acrylic group and the other end coupled to the epoxy group mixed with the initiator, wherein the epoxy group is substantially inert to the UV light. In another aspect of the present invention, the seal patterns may be heated to a temperature of about 120°C for about one hour after being irradiated by the UV light to harden the seal patterns.

[0063] Irradiating the UV light irradiation to the entire surface of the bonded substrates may affect characteristics of devices formed on the substrates (e.g., the thin film transistors) and alter pretilt angles of the alignment layers formed to initially align the liquid crystal material 40. In one aspect of the present invention, a mask (not shown) may be arranged between the UV irradiation apparatus and the bonded substrates 20 and 30 to shield active regions defined by the main seal patterns 70. Although not shown, after the aforementioned UV irradiation process, the bonded

substrates may be cut into individual LCD panels and an inspection process may be performed.

[0064] According to the principles of the present invention, the layer of liquid crystal material may be formed according to a liquid crystal injection method instead of the liquid crystal dispensing method. Therefore, the main and dummy seal patterns 70 and 60 may be formed within the same plane. Moreover, the dummy seal pattern may maintain a substantially uniform cell gap within a non-display (e.g., dummy) region and the main seal patterns may include liquid crystal injection inlets.

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[0065] Figures 4A to 4C illustrate dummy seal patterns according to the principles of the present invention.

[0066] Referring to Figures 4A to 4C, the main seal pattern 70, provided as a plurality of main seal patterns 70, may arranged on the second substrate 30 as a closed shape having no liquid crystal injection inlet while the dummy seal pattern 60 may be provided within the dummy region at the periphery of the main seal patterns 70 in substantially any shape.

provided at the periphery of the main seal patterns 70 in a closed shape. Alternatively, and while referring to Figure 4B, the dummy seal pattern 60 may be provided as a plurality of dummy seal sub-patterns 60 arranged at corners of the second substrate 30 where the main seal pattern 70 is formed. In one aspect of the present invention, the plurality of dummy seal sub-patterns 60 may be provided as "L"-type shapes arranged to surround portions of respective main seal patterns 70. Alternatively, and while referring to Figure 4C, the plurality of dummy seal sub-patterns 60 may be provided as substantially straight-type shapes arranged at outer peripheral sides of respective ones of the main seal patterns 70. In one aspect of the present invention, the dummy seal

patterns 60 shown in Figures 4A to 4C may be formed out of a material including monomers or oligomers each having both ends coupled with an acrylic group mixed with an initiator. In another aspect of the present invention, the dummy seal patterns 60 shown in Figures 4A to 4C may be formed out of a material including monomers or oligomers each having one end coupled with an acrylic group and the other end coupled with an epoxy group mixed with an initiator.

[0068] Figures 5A to 5C illustrate a method of forming a main seal pattern according to a screen printing method in accordance with the principles of the present invention.

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[0069] Referring to Figure 5A, a plurality of alignment marks 31 may be formed on the surface of at least one of the first and second substrates 20 and 30, respectively, (e.g., the second substrate 30). In one aspect of the present invention, at least two alignment marks 31 may be arranged at respective corners of the second substrate 30. In another aspect of the present invention, the at least two alignment marks 31 may be arranged in diagonally opposite corners of the second substrate 30. In another aspect of the present invention, four alignment marks 31 may be arranged at respective corners of the second substrate 30. In yet another aspect of the present invention, four alignment marks 31 may be arranged at corners of liquid crystal display panels within the second substrate 30. In still another aspect of the present invention, the plurality of alignment marks 31 may be provided as various shape-types (e.g., at least one of a +-type shape, a X-type shape, a rectangular shape, a circular shape, etc.).

[0070] Referring to Figure 5B, a screen mask 33 may be arranged over the second substrate 30. Further, the screen mask 33 may be provided with a plurality of openings 32 capable of being aligned over predetermined regions of the second

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substrate 30 using the plurality of alignment marks 31. After aligning the screen mask 33, a sealant material may be disposed over a predetermined portion of the screen mask 33 and a squeegee 34 may be rolled over the screen mask 33 along the arrow direction shown in Figure 5B to force sealant material within the openings 32. Consequently, and as shown in Figure 5C, four main seal patterns 70 may be formed on the second substrate 30 in correspondence to the plurality of openings 32 wherein the four main seal patterns 70 may be used to fabricate four LCD panels. In one aspect of the present invention, the screen mask 33 may be provided as a one piece screen mask as shown in Figure 6A. In another aspect of the present invention, the screen mask 33 may be provided as a division screen mask as shown in Figure 6B.

[0071] Referring to Figure 6A, upon forming the main seal patterns 70 corresponding to the four LCD panels, the main seal patterns 70 may be printed using a one piece screen mask 33 having a plurality of openings 32 used to a form a plurality of main seal patterns 70. According to the principles of the present invention, the size of the one piece screen mask is limited to a maximum size. Therefore, it becomes difficult to effectively use remaining portions of large-sized base substrates (i.e., portions of the base substrate within which LCD panels are not formed) and the cost of manufacturing LCD panels may be excessively large. Further, the size of the one piece screen mask must be increased in accordance with the size of the LCD panel. As the size of LCD panels increases, the size of the one piece screen mask also increases, thereby making the one piece screen mask difficult to maintain and handle.

[0072] Referring to Figure 6B, upon forming the main seal patterns 70 corresponding to the four LCD panels, the main seal patterns may be printed using a division screen mask 33 having a single opening 32 corresponding to dimensions of an LCD panel. According to the principles of the present invention, the main seal pattern

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70 may be arranged on the second substrate 30 corresponding the LCD panel. Subsequently, a sealant material may be disposed over the division screen mask 33 and the main seal pattern 70 may be printed in a region of the second substrate 30 corresponding to a first LCD panel region via a squeegee 34 (as in Figure 5B). Next, the division screen mask 33 may be re-arranged in another region of the second substrate 30 corresponding to a second LCD panel region and another main seal pattern 70 may be printed on the second LCD panel region. By providing the screen mask 33 as a division screen mask, the aforementioned processes may be repeated to form a predetermined number of printed main seal patterns in regions of the second substrate corresponding to LCD panel regions. As a result, any number main seal patterns 70 (e.g., four) may be printed on the within corresponding LCD panel regions of the same base substrate.

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[0073] According to the principles of the present invention, a plurality of LCD panels having different sizes may be formed within the same base substrate using the division screen mask 33. When forming a plurality of LCD panels having substantially the same size within the same substrate, it becomes difficult to efficiently use remaining portions of the base substrate since the size of the remaining portion of the base substrate is typically smaller than the size of the LCD panels that are formed. Accordingly, the efficiency with which the substrate may be used decreases. However, when a plurality of different sized LCD panels (e.g., a first plurality of LCD panels having a first size and a second plurality of LCD panels having a second size, smaller than the first size) are formed within the same base substrate, the efficiency with which the base substrate is used increases. In one aspect of the present invention, the second plurality of LCD panels may be formed within a remaining portion of the

substrate even though the first plurality of LCD panels cannot be formed within the remaining portion.

[0074] A method for fabricating a plurality of different sized LCD panels in accordance with the principles of the present invention will now be described in greater detail below.

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[0075] Figure 7 illustrates an exemplary MMG model including a plurality of different sized LCD panels disposed within the same base substrate.

[0076] Referring to Figure 7, a first plurality of LCD panels 22 having a first size may be formed within a base substrate 21 and a second plurality of LCD panels 23 having a second size, smaller than the first size, may be formed within a remaining portion of the base substrate 21. According to the principles of the present invention, when only the first plurality of LCD panels 22 are formed within the base substrate 21, the remaining portion of the base substrate, having a size suitable for accommodating the second plurality of LCD panels 23 cannot be used and the base substrate 21 is used inefficiently. Accordingly, the first and second plurality of LCD panels, having the different sizes, may be formed within the same base substrate to optimize use of the base substrate. Subsequently, the plurality of main seal patterns 70 may be formed on the base substrate 21 at the periphery of first and second plurality of LCD panels 22 and 23. In one aspect of the present invention, the plurality of main seal patterns 70 may be formed on the base substrate by a printing method. Next, the aforementioned dummy seal pattern 60 may be formed at the periphery of the main seal patterns 70 via a dispensing method wherein sealant material is selectively dispensed directly onto the base substrate 21.

[0077] Figure 8 illustrates UV fixation of a seal pattern according to the principles of the present invention.

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[0078] Referring to Figure 8, the aforementioned seal patterns may be formed of a photo-hardening type sealant material (e.g., a UV hardening-type resin), wherein the main seal patterns 70 seal liquid crystal material between two bonded substrates within the LCD panels, and wherein the dummy seal pattern 60 surrounds the plurality of LCD panels, protects the main seal patterns 70, and maintains a vacuum state between the substrates during the bonding and pressing processes. In one aspect of the present invention, a fixation seal pattern 90 may be provided at fixed intervals at a periphery of the dummy seal pattern 60, proximate edges of the second substrate 30. In another aspect of the present invention, the fixation seal pattern 90 may fix the two bonded substrates and may be removed in a subsequent cutting operation used to divide the bonded substrates into a plurality of LCD panels.

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[0079] After forming the fixation seal patterns 90, the two substrates may be bonded to each other. Subsequently, UV light may be irradiated to the fixation seal patterns 90, thereby hardening the fixation seal patterns 90. As a result, the two substrates bonded to each other are fixed. In one aspect of the present invention, the main seal pattern 70, the dummy seal pattern 60, and the fixation seal pattern 90 may be formed on the second substrate 30. In another aspect of the present invention, the fixation seal pattern 90 may be formed on the first substrate. In still another aspect of the present invention, the fixation seal pattern 90 may be formed of a different material from the main seal pattern 70. In yet another aspect of the present invention, the fixation seal pattern 90 may be formed on the same substrate as the main seal pattern 70 (e.g., the second substrate 30). In still another aspect of the present invention, the fixation seal pattern 90 may be formed of the different material from the main seal pattern 70 and the fixation seal pattern 90 may be formed of the different material from the main seal pattern 70 and the fixation seal pattern 90 may be formed on a different

substrate from the main seal pattern 70 or on the same substrate as the main seal pattern 70 (e.g., the second substrate 30). In one aspect of the present invention, the main seal pattern 70 and the liquid crystal may be formed on the first substrate 20 while the fixation seal pattern 90 may be formed on the second substrate 30. In another aspect of the present invention, the fixation seal pattern 90 and the liquid crystal material may be formed on the first substrate 20, and either the main or dummy seal pattern 70 or 60 may be formed on the second substrate 30. In still another aspect of the present invention, the fixation seal pattern 90 may be formed via the aforementioned dispensing method whereby sealant material is selectively dispensed onto the substrate. In yet another aspect of the present invention, the fixation seal pattern 90 may be formed at the periphery of the dummy seal pattern 60 or substantially collinear with the dummy seal pattern 60.

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[0080] The aforementioned method of manufacturing LCD devices in accordance with the present invention is advantageous because the main seal pattern may be formed by a screen printing method and the dummy seal pattern may be formed by a dispensing method. Accordingly, contamination of the liquid crystal material may be prevented, thereby improving yield and device reliability. Further, main seal patterns may be printed onto a base substrate (e.g., a large-sized substrate) using a division screen mask or a one piece screen mask. Still further, main seal patterns corresponding to LCD panels of different sizes may be formed on the same base substrate. Moreover, a tact time may be decreased.

[0081] It will be apparent to those skilled in the art that various modifications and variations can be made in the present invention. Thus, it is intended that the present invention covers the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.